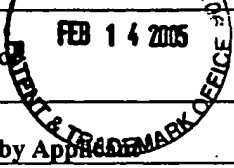
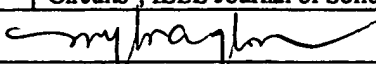


Form PTO 1449 (Rev. 2-32)		U.S. Department of Commerce Patent and Trademark Office				Atty. Docket No. IMPJ-0031		Serial No. 10/814,867	
Information Disclosure Statement by Applicant						Applicant: Frederic Bernard et al.			
(Use several sheets if necessary)						Filed: March 30, 2004		Group: 2816	
U.S. Patent Documents									
Init.		Document No.	Date	Name	Class	Subclass	Filing Date		
MT	A	2003/0206437	11/6/2003	Diorio et al.	365	185.03			
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MT	D	2004/0037127	2/26/2004	Lindhorst et al.	365	202			
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Init.		Document No.	Date	Country	Class	Subclass	Yes	No	
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MT	T	Invitation to Pay Additional Fees (Partial International Search), Application No. PCT/US 03/31792, date of mailing April 22, 2004.							
MT	U	Declercq, et al., "Design and Optimization of High-Voltage CMOS Devices Compatible with a Standard 5 V CMOS Technology", IEEE Custom Integrated Circuits Conference, 1993, pp. 24.6.1-24.6.4							
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Examiner 					Date Considered 10/13/05				
Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.									

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Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office				Atty. Docket No. IMPJ-0031		Serial No.: Unassigned 10/814,867		
Information Disclosure Statement by Applicant				Applicant: Frederic Bernard et al.				
(Use several sheets if necessary)				Filed: <u>Herewith</u> Group: Unassigned 3/30/04 2816				
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Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.								

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